IN THE SPECIFICATION

Presented below are specification changes showing the changes made.

Please replace paragraph [0001] with the following amended paragraph:

[0001] This application claims the benefit of U.S. Provisional Patent Application No. 60/257,275, entitled "Dual-Line ADSL Modem," filed December 21, 2000. This application is also related to U.S. Patent Application No. 09/791,970, entitled "Multi-Line ADSL Modulation," filed February 22, 2001, now U.S. Patent No. 6,507,608. The subject matter of the related applications is hereby incorporated by reference. The related applications are commonly assigned.

Please replace paragraph [0009] with the following amended paragraph:

[0009] Customer premises 104 includes, but is not limited to, an ADSL Transceiver Unit-Remote (ATU-R) 132 (also sometimes called an ADSL modem), a personal computer (PC) 134, and a telephone 138. Splitter 130 splits the signals from loop 106, sending the DSL signals via path 140 to ATU-R 132 and the POTS signals via path 144 to telephone 138. ATU-R 132 processes the DSL signals and sends the resulting data, for example a web page or email message, to PC 134 via path 142. Splitter 130 also receives signals from ATU-R 132 and telephone 138, and combines the signals for transmission over loop 106. DSLAM 103 includes an Asynchronous Transfer Mode (ATM) layer for processing data sent and received via DSL signals.

Please replace paragraph [0011] with the following amended paragraph:

[0011] Splitter 230 splits the signals on loop 206, sending DSL signals via path 242 to ATU-R 232 and POTS signals via path 236 to telephone 238. Splitter 230 also splits the signals on loop 208, sending DSL signals via path 240 to ATU-R 232 and POTS signals via path 246 to telephone 238. Although not shown in FIG. 2, other POTS devices such as facsimile machines and dial-up modems may send and receive signals via path 236 and path 246. ATU-R 232 processes DSL signals received via path 240 and 242 to produce data that is output to PC 234 via path 244, and processes data received from PC 234 to produce DSL signals that are output to path 240 and path 242.